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Overview. The IEEE 1364-2001 standard, nicknamed 'Verilog-2001', is the first major update to the Verilog language since its inception in 1984. This book presents 45 significant enhancements contained in Verilog-2001 standard. A few of the new features described in this book are: ANSI C style port declarations for modules, primitives, tasks and functions; Automatic tasks and functions (re-entrant tasks and recursive functions); Multidimensional arrays of any data type, plus array bit and ...

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Verilog-2001 Quick Reference Guide

Introducing the Verilog HDL in a brief format, this book presents a selected set of the changes the popular hardware underwent in its first revision—emerging as IEEE Std 1364-2001 or Verilog-2001. It addresses the main features that support the design of combinational and sequential logic, and emphasizes synthesizable models, with a limited discussion of the theoretical framework for synthesis.

Starter's Guide to Verilog 2001: 9780131415560: Computer ...

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Ciletti, Starter's Guide to Verilog 2001 | Pearson

IEEE 1364-2001 Verilog standard. Today, Verilog has become the language of choice for digital design and is the basis for synthesis, verification, and place and route technologies. Samir's book is an excellent

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guide to the user of the Verilog language. Not only does it

Verilog HDL: A Guide to Digital Design and Synthesis, 2nd Ed.

Verilog HDL: A Guide to Digital Design and Synthesis, Second Edition By Samir Palnitkar Publisher: Prentice Hall PTR Pub Date: February 21, 2003 ISBN: 0-13-044911-3 Pages: 496 Written for both experienced and new users, this book gives you broad coverage of Verilog HDL The book stresses the practical design and verification perspective of Verilog rather than emphasizing only the language aspects The information presented is fully compliant with the IEEE 1364-2001 Verilog HDL standard ...

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Verilog HDL: A Guide to Digital Design and Synthesis, Second Edition The book stresses the practical

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ABSTRACT The Verilog-2001 Standard includes a number of enhancements that are targeted at simplifying designs, improving designs and reducing design errors. This paper details important

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enhancements that were added to the Verilog-2001 Standard that are intended to simplify behavioral modeling and to improve synthesis accuracy and efficiency.

Verilog-2001 Behavioral and Synthesis Enhancements

Jim Duckworth, WPI 3 Verilog Module Rev B Books □ □FPGA Prototyping by Verilog Examples□, 2008, Pong P. Chu, Wiley 978-0-470-18532-2 □ □Starters Guide to Verilog 2001□ by Ciletti, 2004, Prentice Hall 0-13-

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