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Architecture Chapter 5 - Basic Computer Organization and Design

PG-TRB COMPUTER

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Previous Year Questions on Computer System
Architecture (Part 1) Basics of OS (Computer
System Operation)

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Computer Instructions

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Architecture chap 8 program control~~ Inside
your computer - Bettina Bair

**How computer
memory works - Kanawat Senanan ? - See How a
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Computer Architecture What is SYSTEMS
ARCHITECTURE? What does SYSTEMS ARCHITECTURE
mean? Introduction to Operating System

RAM
Explained - Random Access Memory

Bit
Manipulation 04: Bit Masking **33 addition and
subtraction of floating point numbers**

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P. V. S. Rao. Paranandi Venkata Suryanarayana Rao is an Indian computer scientist, known for his researches in the fields of speech and script recognition and is credited with contributions for the development of TIFRAC, the first indigenously developed electronic computer in India. He is a recipient of awards such as IEEE Third Millennium Medal, Vikram Sarabhai Award, Om Prakash Bhasin Award and VASVIK Industrial Research Award.

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Rao, A. S., Georgeff, M. P. (1993) A model-theoretic approach to the verification of situated reasoning systems. In: Proceedings of the Thirteenth International Joint Conference on Artificial Intelligence (IJCAI-93), Chamberey, France.

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nical Report 250, Computer Laboratory,
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