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i.e., build the prototype, without any extensive requirements determination and then use the prototype to define or refine the requirements.

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Understanding of the growing and ever-adapting field. Academics, researchers, students, and working professionals in this field will benefit from this publication's unique perspective.

DIGITAL SYSTEMS DESIGN USING

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VERILOG integrates coverage of logic design principles, Verilog as a hardware design language, and FPGA implementation to help electrical and computer engineering students master the process of designing and testing new hardware configurations. A Verilog equivalent of authors Roth and

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John's previous successful text using VHDL, this practical book presents Verilog constructs side-by-side with hardware, encouraging students to think in terms of desired hardware while writing synthesizable Verilog. Following a review of the basic concepts of logic design, the authors

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